



JNIESTR TRUST'S
SMT INDIRA GANDHI COLLEGE OF ENGINEERING
An Autonomous Institute with NAAC 'A' Grade
Approved by AICTE, Affiliated to the University of Mumbai
DEPARTMENT OF ELECTRICAL ENGINEERING



SR. No.: SIGCE/ELECT/Circular/21 /2026-27

Date : 01/10/2026

NOTICE

All the faculties and students are hereby inform that Unit Test-II will be scheduled on date 8, 9 and 12 October 2026.

Div	08/10/2026		09/10/2026	12/10/2026	
	10:30-11:30 AM	2.00 to 3.00PM	2.00 to 3.00PM	10:30-11:30 AM	2.00 to 3.00PM
SE	AMEE	FINTECH	EEM	EM-I	EPS-I
TE	CS	PPT/UEE/APEC	PS	REES	CAD/AADS
BE	MGSG	HVDC/IOT	EPS- III	EDC	DMMM/EAM

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