



JNIESTR TRUST'S
SMT INDIRA GANDHI COLLEGE OF ENGINEERING
An Autonomous Institute with NAAC 'A' Grade
Approved by AICTE, Affiliated to the University of Mumbai
DEPARTMENT OF ELECTRICAL ENGINEERING



SR. No.: SIGCE/ELECT/Circular/ 11 /2026-27

Date : 17/08/2026

NOTICE

All the faculties and students are hereby inform that Unit Test-I will be scheduled on date 21, 24 and 25 August 2026.

Div	21/08/2026	24/08/26		25/08/26	
	2.00 to 3.00 PM	10:30-11:30 AM	2.00 to 3.00PM	10:30-11:30	2.00 to 3.00PM
SE	EM-I	AMEE	FINTECH	EPS-I	EEM
TE	CS	PS	PPT/UEE/APEC	RES	CAD/AADS
BE	EPS-III	EDC	HVDC/IOT	MSGG	DMMM/EAM

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