



JNIESTR TRUST'S
SMT INDIRA GANDHI COLLEGE OF ENGINEERING

An Autonomous Institute with NAAC 'A' Grade
Approved by AICTE, Affiliated to the University of Mumbai

DEPARTMENT OF COMPUTER ENGINEERING



SR. No.: SIGCE/COMP/10/2026-27

Date: 08/09/2026

NOTICE

Parents of SE, TE and BE Computer Engineering students are hereby invited for Odd Semester 2026-27 Parent-Teacher Meeting (PTM).

Parents can interact with teachers and know the performance of their respective wards. Suggestions/ feedback of the parents will help the department for better nurturing of the students.

Date: September 11th, 2026

Venue: Room no 515, 5th Floor, Smt. Indira Gandhi College of Engineering, Ghansoli.

Mode: Hybrid (Online/Offline)

Time: SE A – 10:00 am to 10:45 am

SE B – 10:45 am to 11:00 am

TE – 11:30 am to 12:15 pm

BE – 12:15 am to 1:00 pm

Dr. Madhumita Bhattacharjee
PTM Coordinator

Dr. K. T. Patil
HOD, Comp. Engg Dept.