



JNIESTR TRUST'S
SMT INDIRA GANDHI COLLEGE OF ENGINEERING

An Autonomous Institute with NAAC 'A' Grade
Approved by AICTE, Affiliated to the University of Mumbai

Computer Engineering Department



Training and Placement Cell

SR. No.: SIGCE/Comp/09/2026-27

Date: 27/08/2026

NOTICE

All the students of Computer Engineering department hereby inform that our department is conducting **Value added program on Next- Gen AI Architect: Mastering LLMs, RAG and Agentic Workflows** for enhancing your skill Scheduled **from 31-08-2026 to 04-09-2026**.

Students are informed give your name to Dept T&P In-Charge on or before 28-08-2026 given Schedule.

Venue: Lab 503 & 504

- Attendance is Compulsory
- After Successful completion of the program, a certificate will be issued.

Sr. No	Branch	Date	Time
1	Computer Engineering	31 st August 2026	10.00 am to 5.00 pm
		1 st September 2026	10.00 am to 5.00 pm
		2 nd September 2026	10.00 am to 5.00 pm
		3 rd September 2026	10.00 am to 5.00 pm
		4 th September 2026	10.00 am to 5.00 pm

Prof. Selvamurugan Kasi
T&P In-charge

Dr. K.T. Patil
HOD

Dr. Sunil S Chavan
Principal



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