

JNIESTRT  
**SMT. INDIRA GANDHI COLLEGE OF ENGINEERING**

An Autonomous Institute with NAAC 'A' Grade  
Approved by AICTE, Affiliated to the University of Mumbai.



**DEPARTMENT OF EXAMINATION**

**RE-EXAMINATION TIME TABLE**

**PROGRAM SE ELECTRICAL ENGINEERING**

**SEMESTER-III (ODD) SIGCE NEP 2020 Scheme**

**Winter Session 2025**

Sr. No.	Days and Dates	Time	Subject Code	Paper
1	Saturday, 20 December, 2025	2:30 PM to 5:00 PM	2223111	Engineering Mathematics for Electrical Circuits
2	Tuesday, 23 December, 2025	2:30 PM to 5:00 PM	2223112	Electrical Machines - I
3	Friday, 26 December, 2025	2:30 PM to 5:00 PM	2223113	Electronics Engineering
4	Tuesday, 30 December, 2025	2:30 PM to 5:00 PM	2223114	Measurements and Measuring Instruments
5	Thursday, 01 January, 2025	2:30 PM to 5:00 PM	OEC301	Open Elective (Finance Management)

**Important  
Note:**

- The candidates appearing for the examination should report 20 minutes before the start of examination.
- Mobile phones and other electronic gadgets are prohibited in the examination hall.
- Change, if any, in the time table shall be communicated on the SIGCE web site.

Navi Mumbai – 400 701

2<sup>nd</sup> December, 2025

Prof. Nishant B. Joshi

Controller of Examinations



Dr. Sunil S Chavan

**Principal  
Principal**

Smt. Indira Gandhi College of Engineering  
Plot No. 1, Sector-08,  
Ghansoli, Navi Mumbai-400701.